WHAT IS CLAIMED IS:

1. An electro-optical device comprising:

a first substrate having thereon a display region and a drive circuit region comprising a drive circuit for controlling the display in said display region; and

a second substrate opposed to said first substrate,

wherein said regions are partitioned by a sealing agent, and a liquid crystal material is incorporated between said first substrate and said second substrate, and said second substrate is extended to oppose both of said display region and said drive circuit region provided on the first substrate.

2. The device of claim 1 wherein a drive circuit is provided on the second substrate at a region opposed to the drive circuit region.

3. The device of claim 1 wherein said display region comprises an amorphous silicon transistor, and said drive circuit region comprises a crystalline silicon transistor.

4. The device of claim 1 wherein said display region comprises a MIM diode.

5. The device of claim 1 wherein said display region comprises a simple matrix electrodes.

6. An electro-optical device comprising:

a first substrate having thereon a display region and a drive circuit region comprising a drive circuit for controlling the display in said display region; and

a second substrate opposed to said first substrate,

wherein said regions are partitioned by a sealing agent, and a liquid crystal material is incorporated between said first substrate and said second substrate, and said second substrate is extended to oppose both of said display region and said drive circuit region provided on the first substrate, and at least a part of the periphery of said drive circuit region has thereon a sealing agent.

7. The device of claim 6 wherein a drive circuit is provided on the second substrate at a region opposed to the drive circuit region.

8. The device of claim 6 wherein said display region comprises an amorphous silicon transistor, and said drive circuit region comprises a crystalline silicon transistor.

9. The device of claim 6 wherein said display region comprises a MIM diode.

10. The device of claim 6 wherein said display region comprises a simple matrix electrodes.

11. An electro-optical device comprising:

a first substrate having thereon a display region and a drive circuit region comprising a drive circuit for controlling the display in said display region; and

a second substrate opposed to said first substrate,

wherein said regions are partitioned by a sealing agent, and a liquid crystal material is incorporated between said first substrate and said second substrate, and said second substrate is extended to oppose both of said display region and said drive circuit region provided on the first substrate, and a resin material is charged at least between said second substrate and said drive circuit region.

12. The device of claim 11 wherein a drive circuit is provided on the second substrate at a region opposed to the drive circuit region.

13. The device of claim 11 wherein said display region comprises an amorphous silicon transistor, and said drive circuit region comprises a crystalline silicon transistor.

14. The device of claim 11 wherein said display region comprises a MIM diode.

15.The device of claim 11 wherein said resin material comprises a material selected from the group consisting of an epoxy resin and an ultraviolet hardening resin.

16. An electro-optical device comprising:

a first substrate having thereon a display region and a drive circuit region comprising a drive circuit for controlling the display in said display region; and

a second substrate opposed to said first substrate,

wherein said regions are partitioned by a sealing agent, and a liquid crystal material is incorporated between said first substrate and said second substrate, and said second substrate is extended to oppose both of said display region and said drive circuit region provided on the first substrate, and at least a part of the periphery of said drive circuit region has thereon a sealing agent, and a resin material is charged at least between said second substrate and said drive circuit region.

17. The device of claim 16 wherein a drive circuit is provided on the second substrate at a region opposed to the drive circuit region.

18. The device of claim 16 wherein said display region comprises an amorphous silicon transistor, and said drive circuit region comprises a crystalline silicon transistor.

19. The device of claim 16 wherein said display region comprises a MIM diode.

20.The device of claim 16 wherein said resin material comprises a material selected from the group consisting of an epoxy resin and an ultraviolet hardening resin.